

**IN THE SPECIFICATION:**

Please amend paragraph [0039] as follows.

[0039] FIG. 5 is a plan view showing a pixel of the liquid crystal display device including a storage capacitor electrode. As shown in FIG. 5, even if data lines 315n and 315n+1 protrude into the pixel area above the storage capacitor electrode 525, the aperture ratio is not decreased. By protruding the data lines 315n and 315n+1 into the pixel area over the storage capacitor electrode 525 [[340]] at intersections between the data lines and the storage capacitor electrode 525 [[340]], the overlapped areas at the right and left sides of the pixel electrode are controlled to be substantially equal to one another. That is, by protruding the right and left data lines 315n and 315n+1 into the pixel area above the storage capacitor electrode 525 [[340]] and thus varying the area S1 and area S2, a value of the parasitic capacitance for both of the S1 and S2 areas can be adjusted to be substantially equivalent. As shown in FIG. 5, region E denotes the increased overlapped area for the data line 315n of the corresponding pixel area and region E' denotes the increased overlapped area for the data line 315n+1 of the adjacent pixel area. As shown in FIG. 5, since the d1 is less than the d2 and thus the area S1 is smaller than the area S2, an area of a part 350 protrudes into the corresponding pixel area from the n<sup>th</sup> data line 315n with a length  $\ell_1$  that is larger than an area of a part 355 protruding into the corresponding pixel area from the n+1<sup>th</sup> data line 315n+1[[n]] with a length  $\ell_2$ . That is,  $\ell_1$  is longer than  $\ell_2$ , so that the overlapped areas of S1' and S2' are substantially equal.